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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

none full

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN JPAP 2003-17506 01/27/2003

yes full

IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\*

04/27/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>[Signature]</i> Initials			

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## TITLE

Semiconductor integrated circuit and amplifier for suppressing pop sound while minimizing voltage transition settling time

<b>FILING FEE RECEIVED</b> 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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